

FIG.2

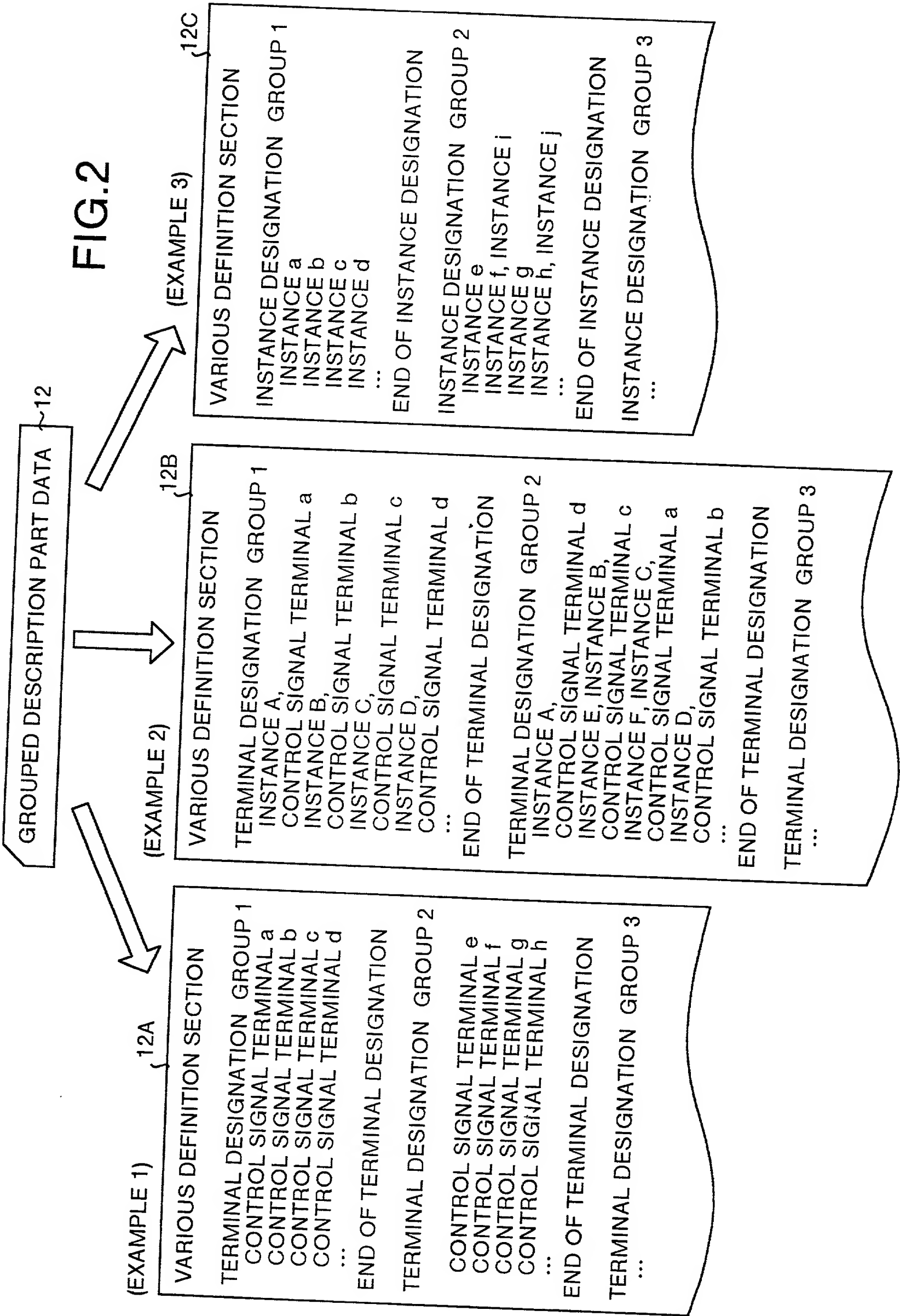


FIG. 3

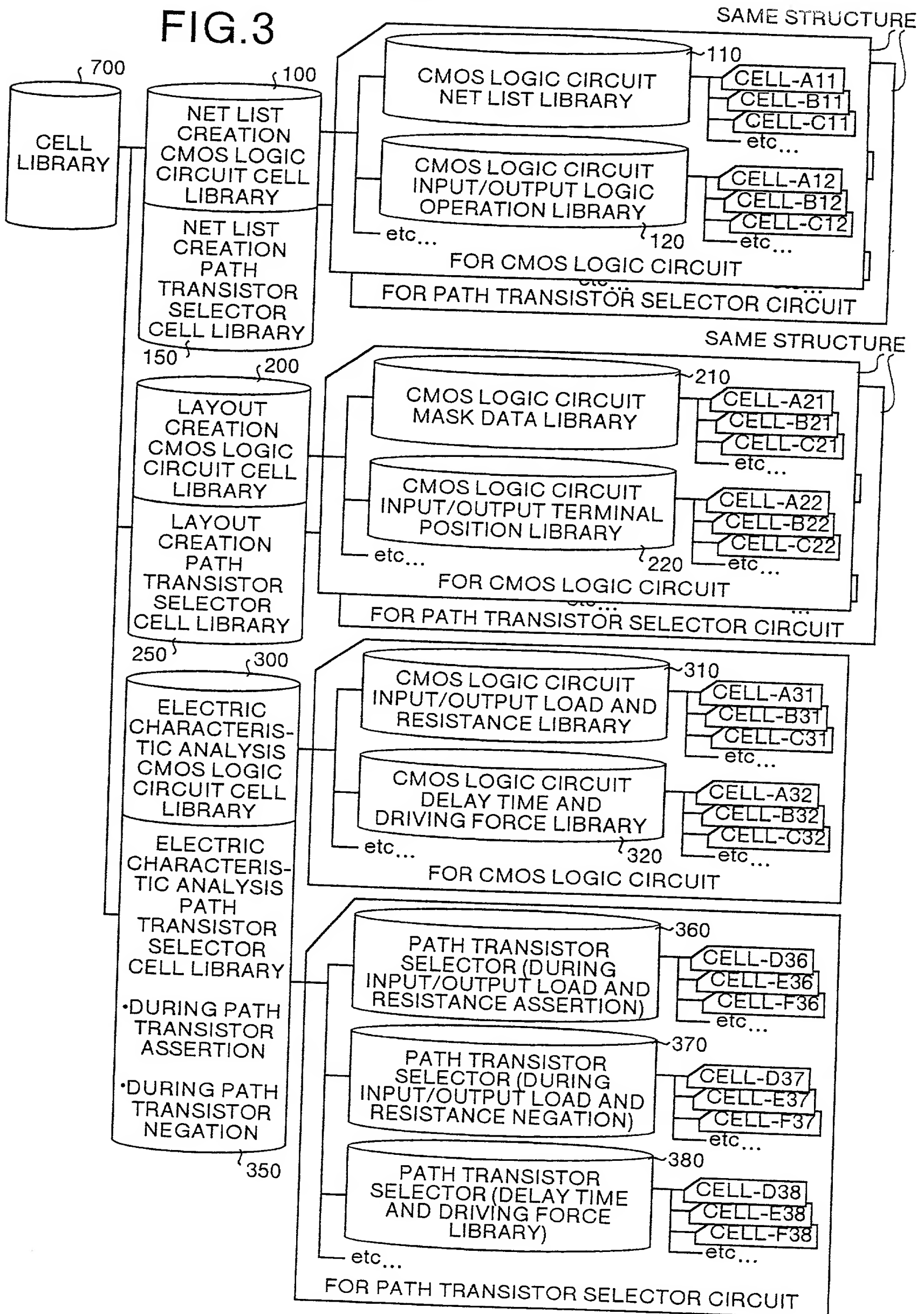


FIG.4

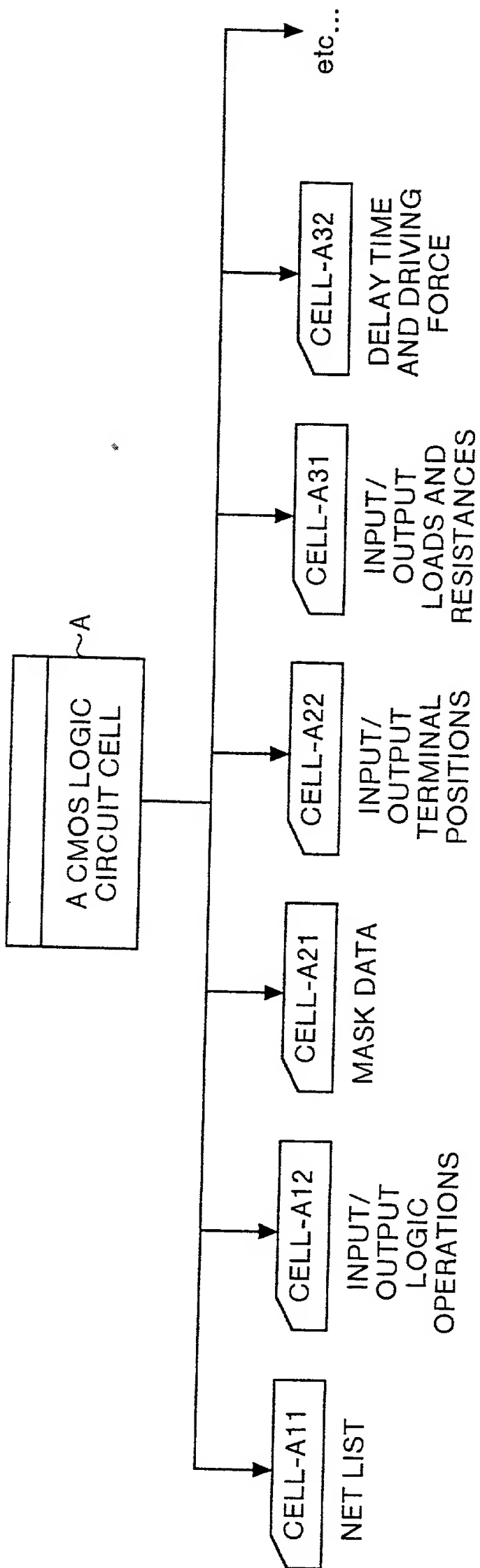


FIG.5

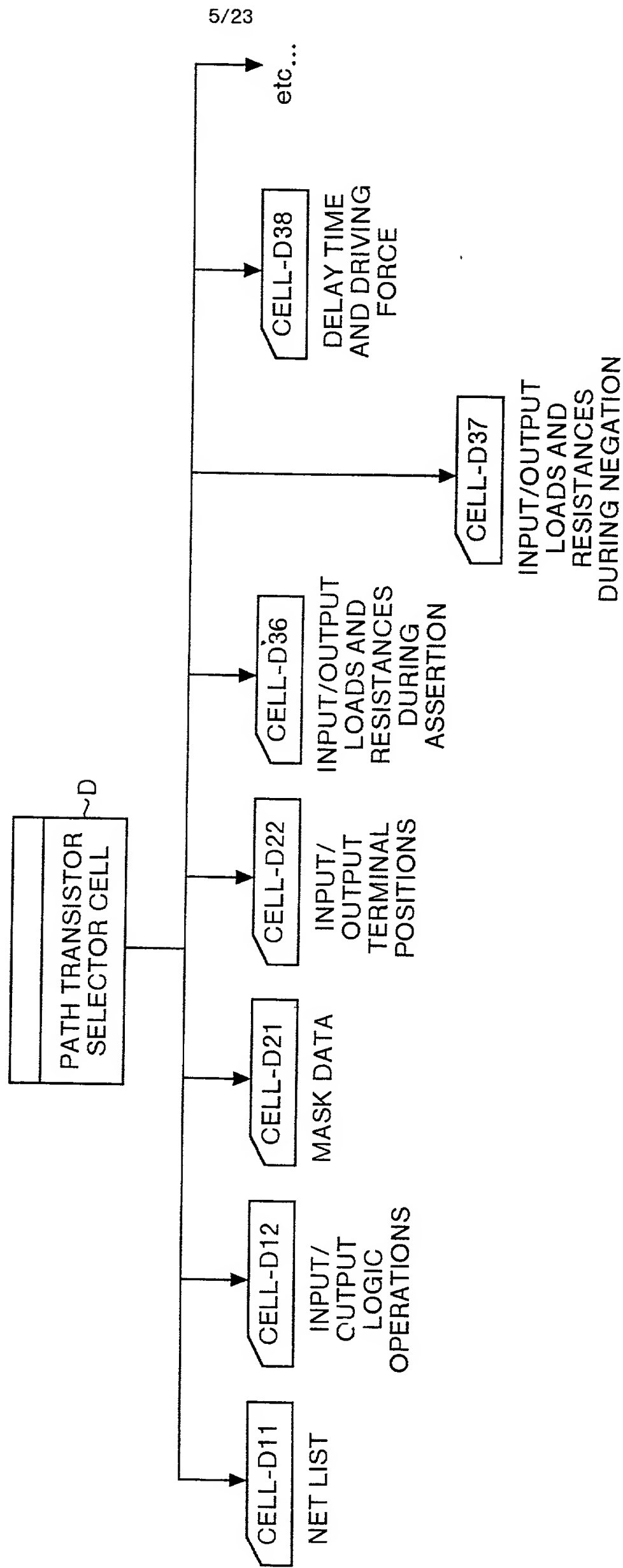


FIG.6A

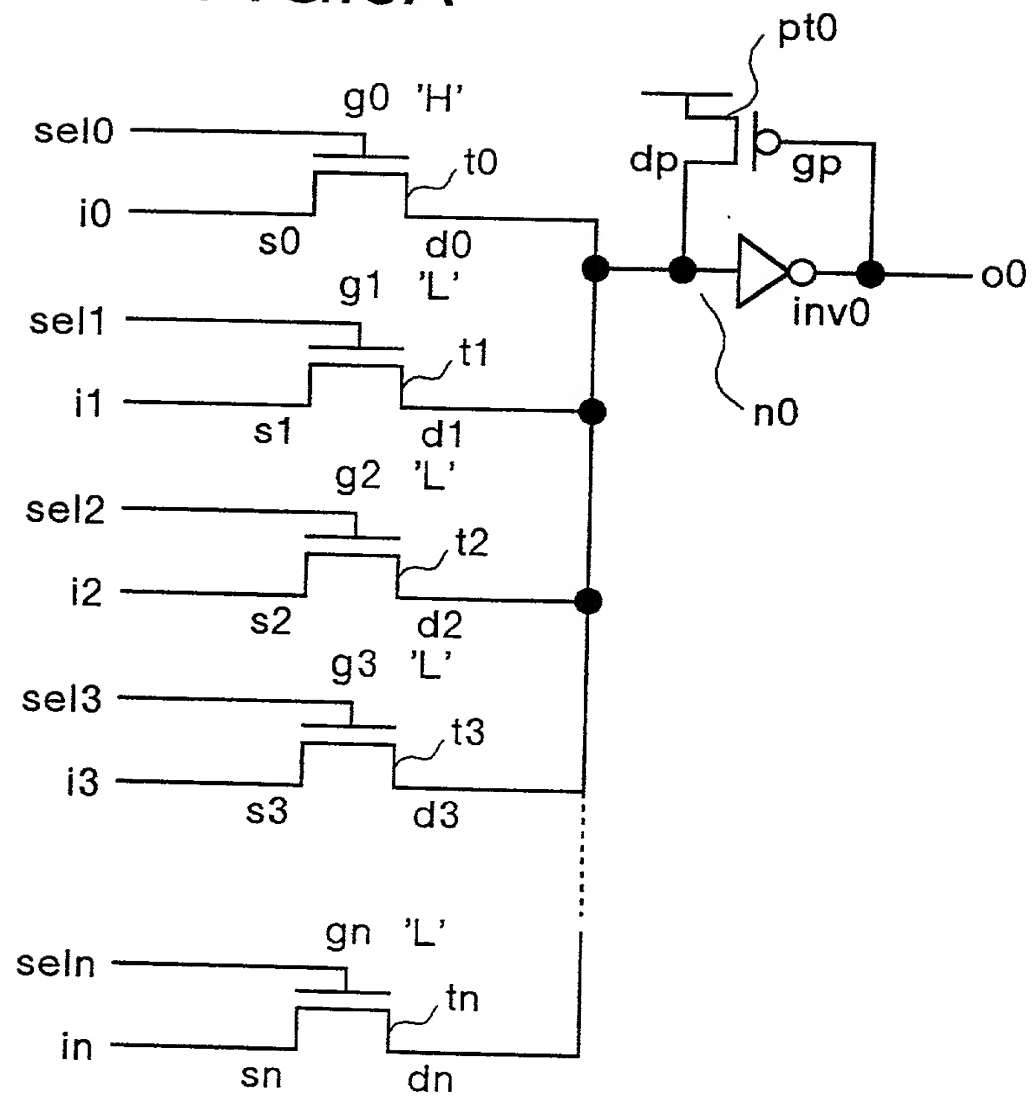
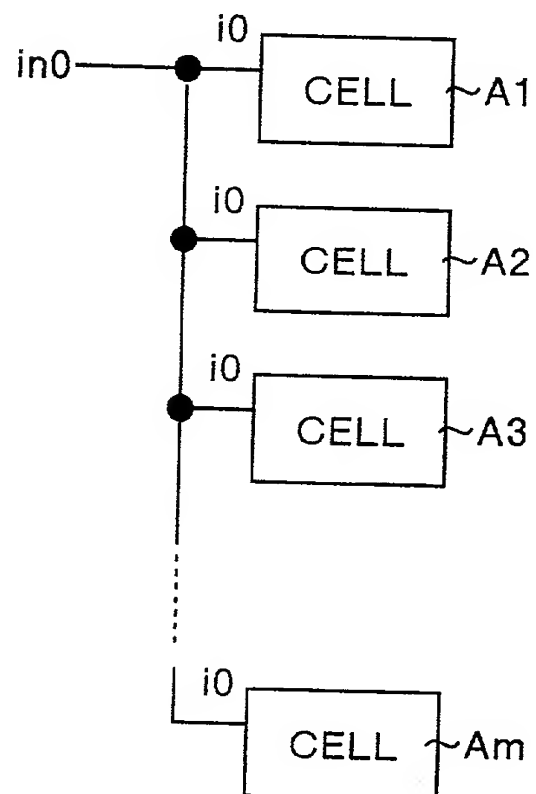


FIG.6B



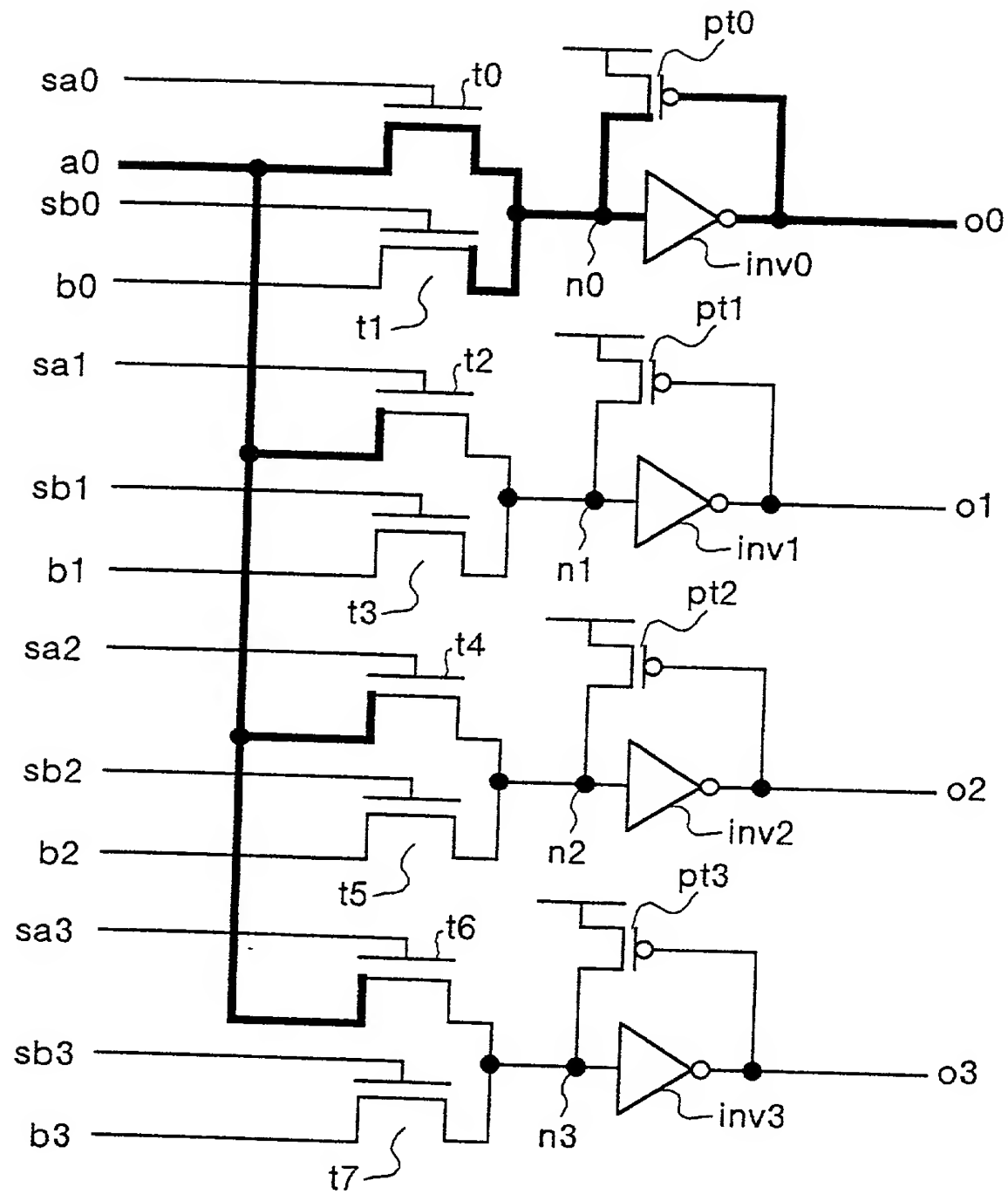
[illegible]

FIG.8

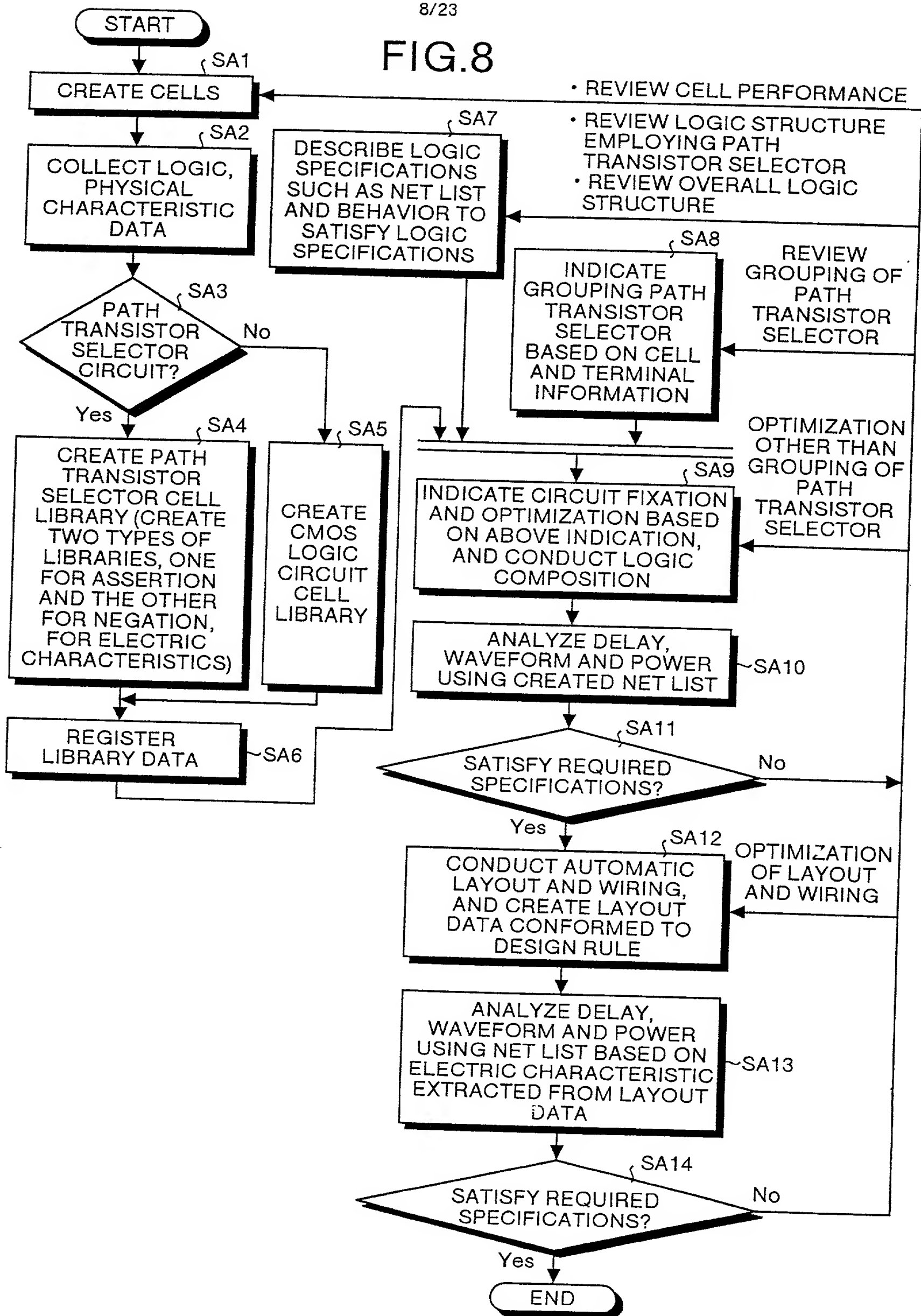


FIG.9

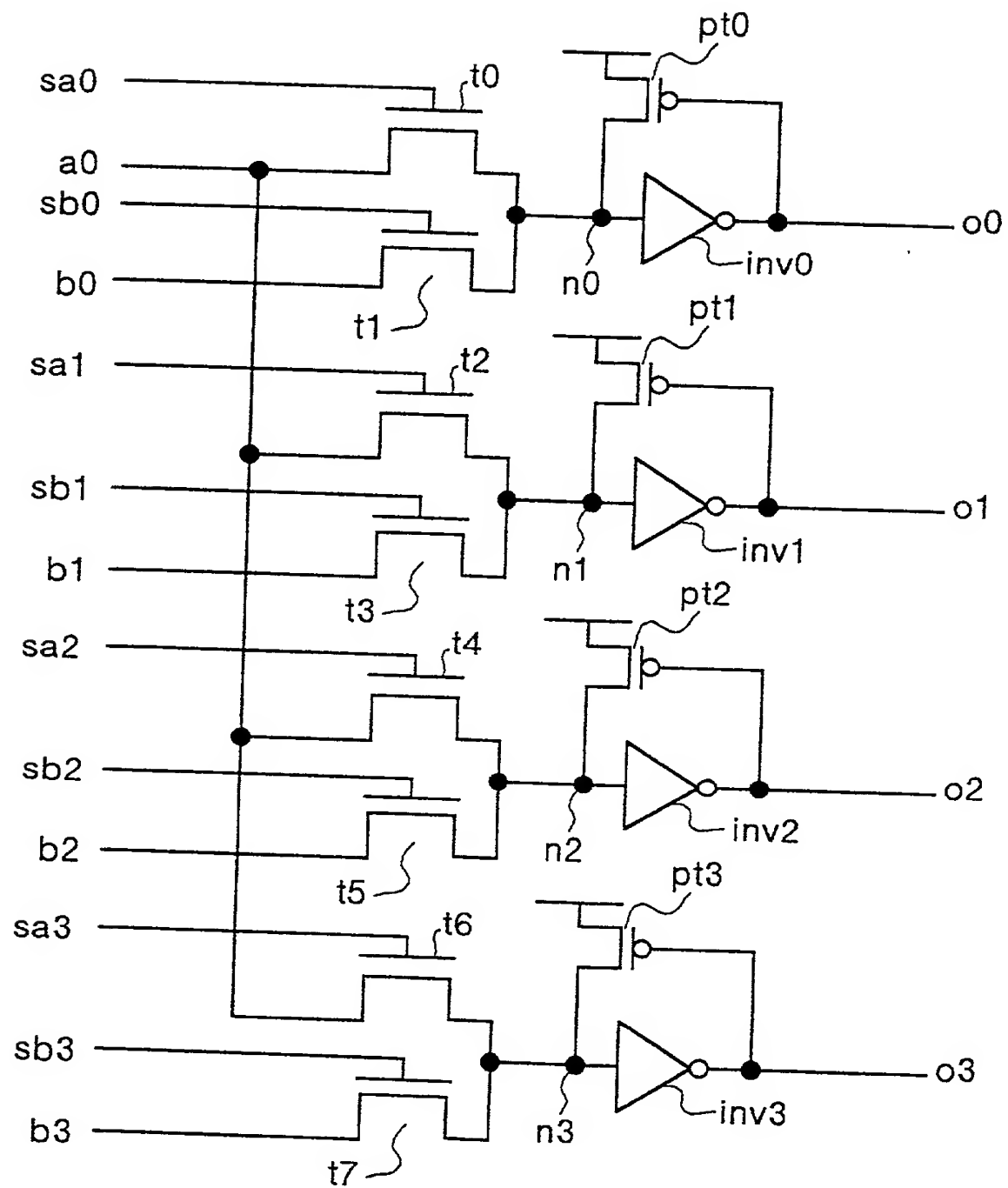


FIG.10

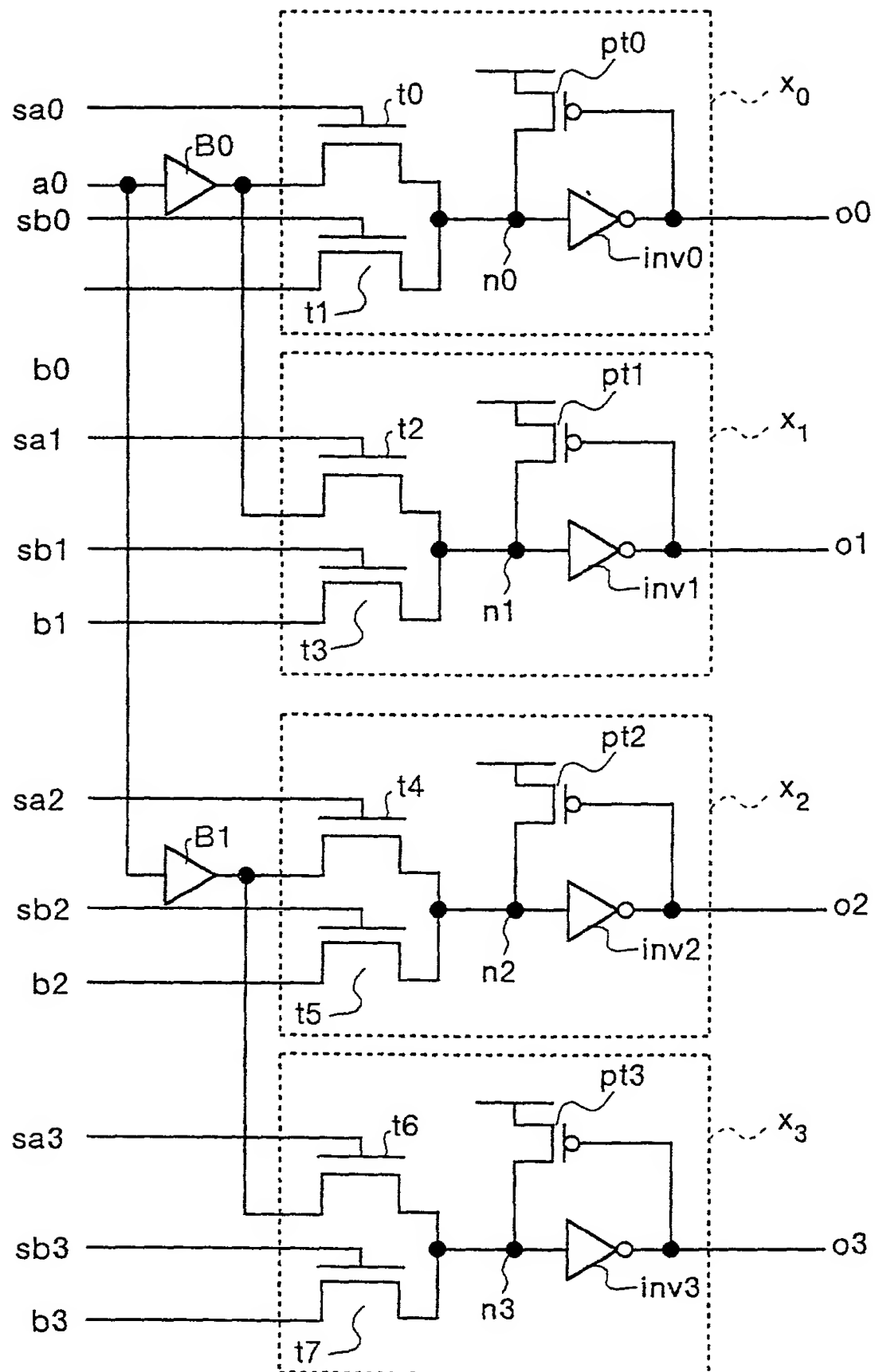


FIG.11A

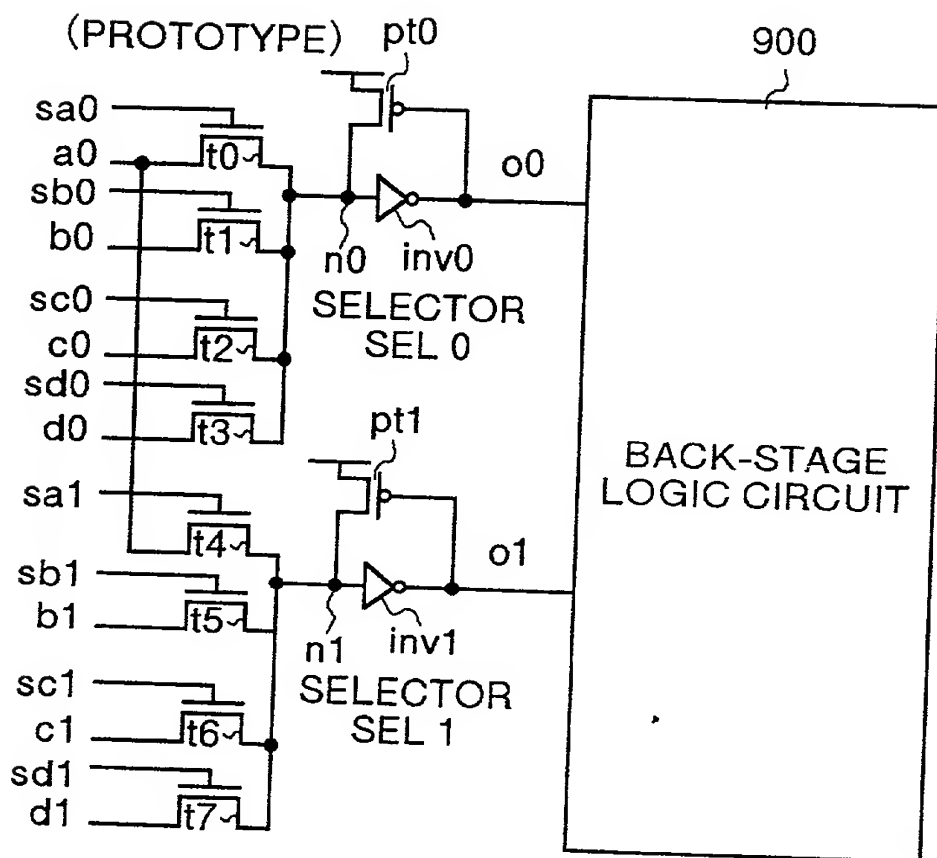


FIG.11B

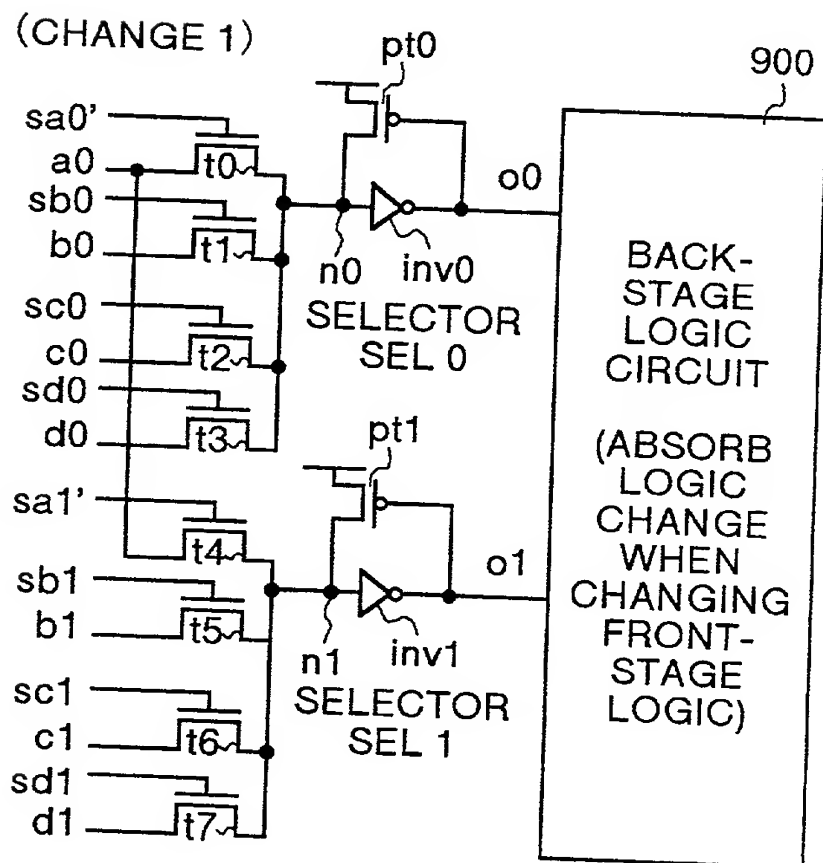


FIG.11C

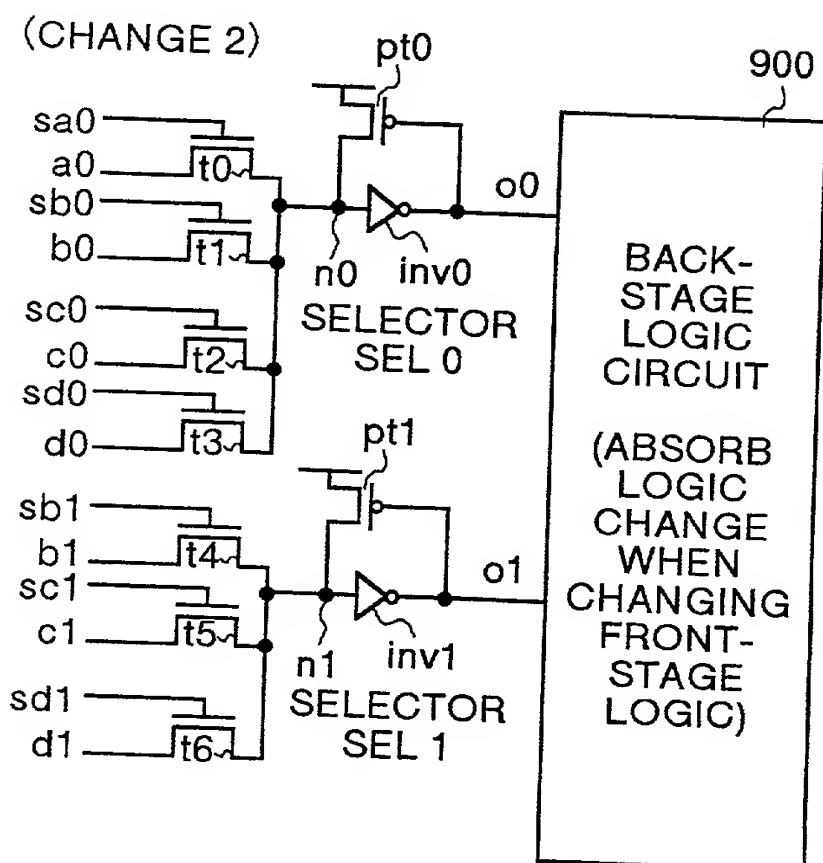


FIG.12A

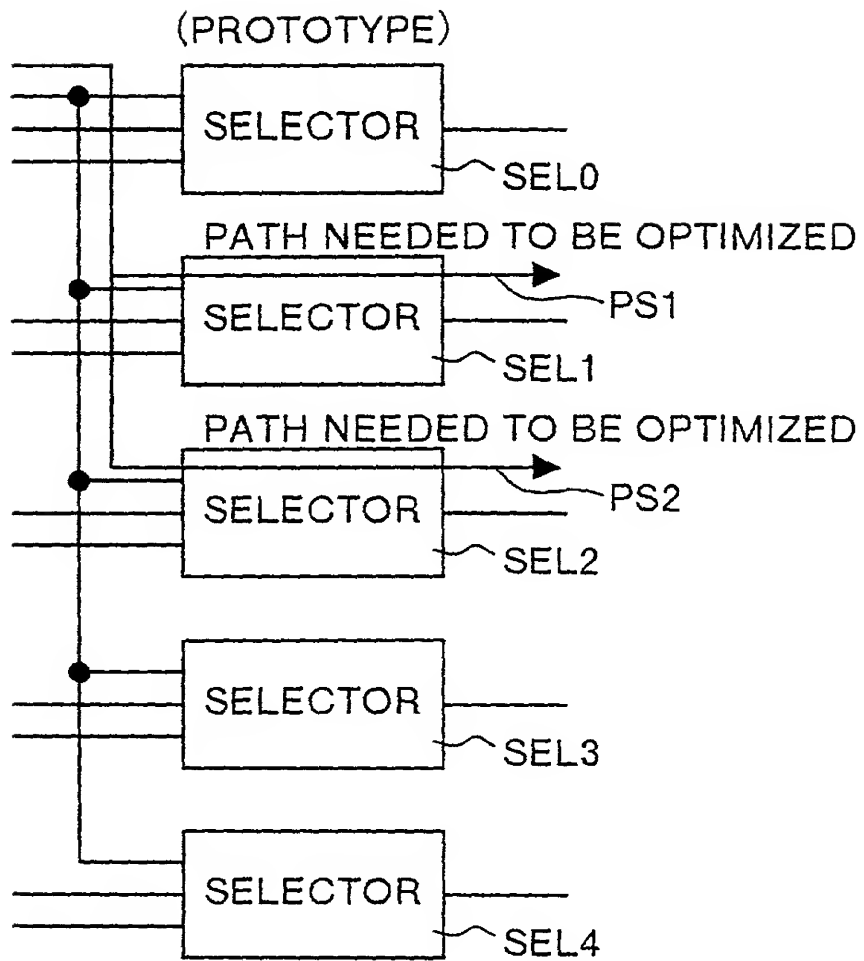


FIG.12B

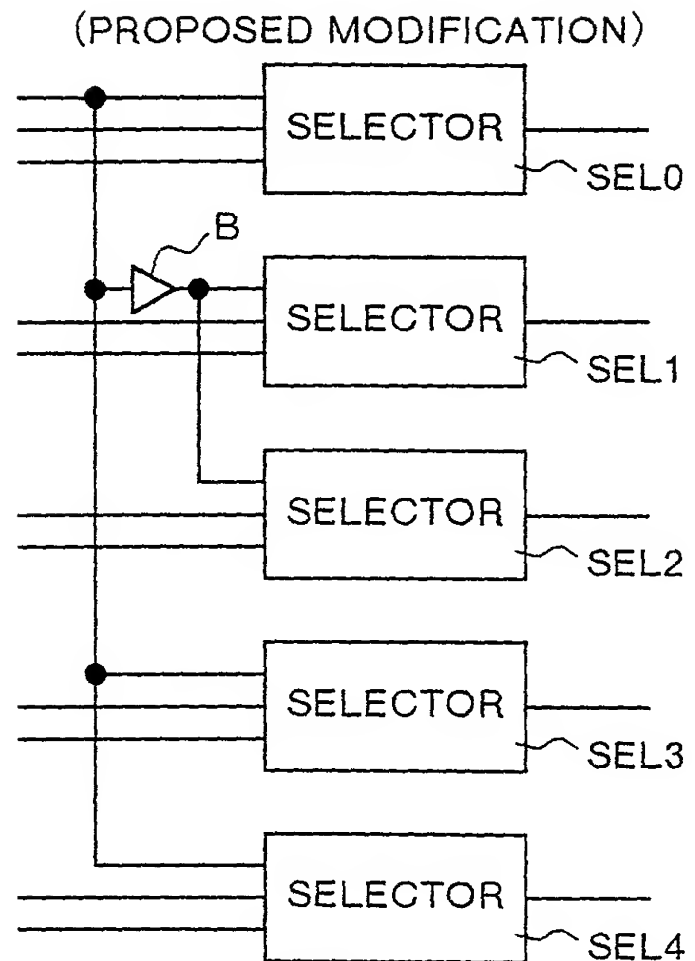


FIG.13A

(PROTOTYPE)

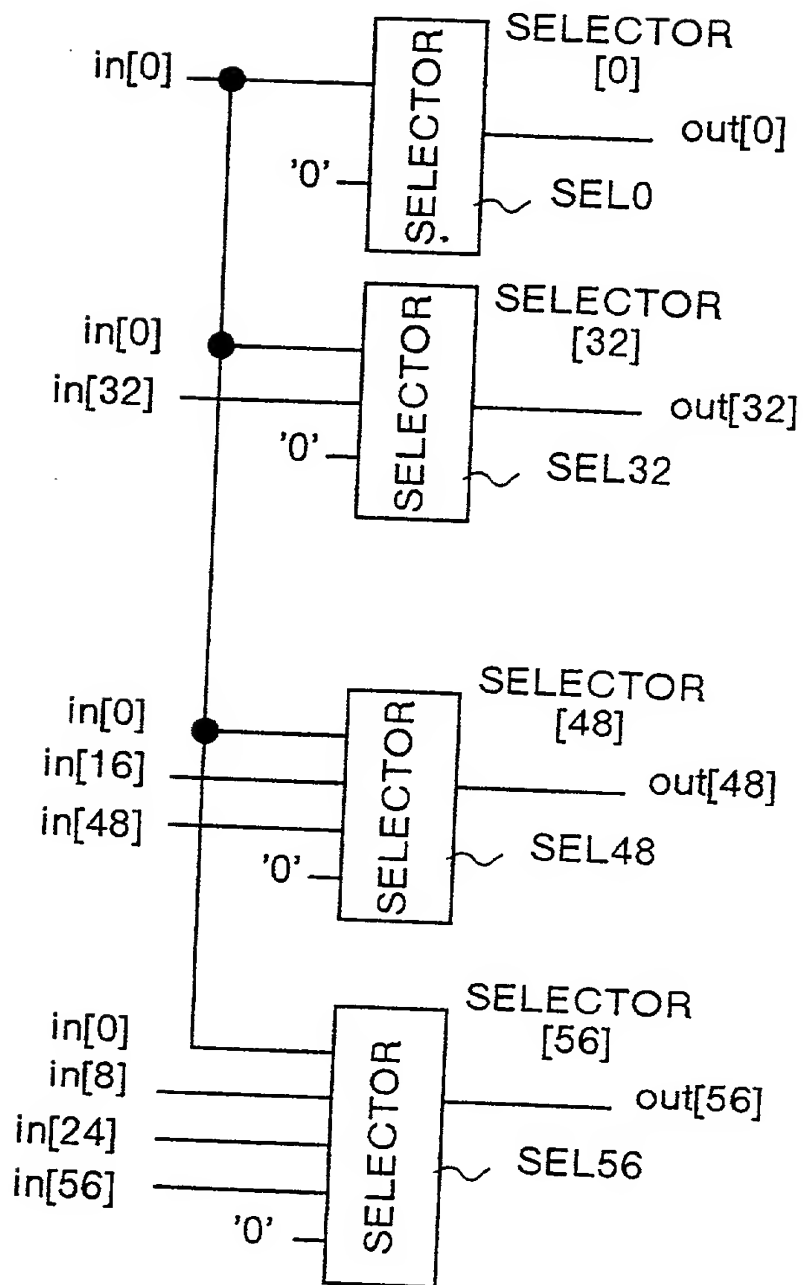


FIG.13B

(PROPOSED MODIFICATION)

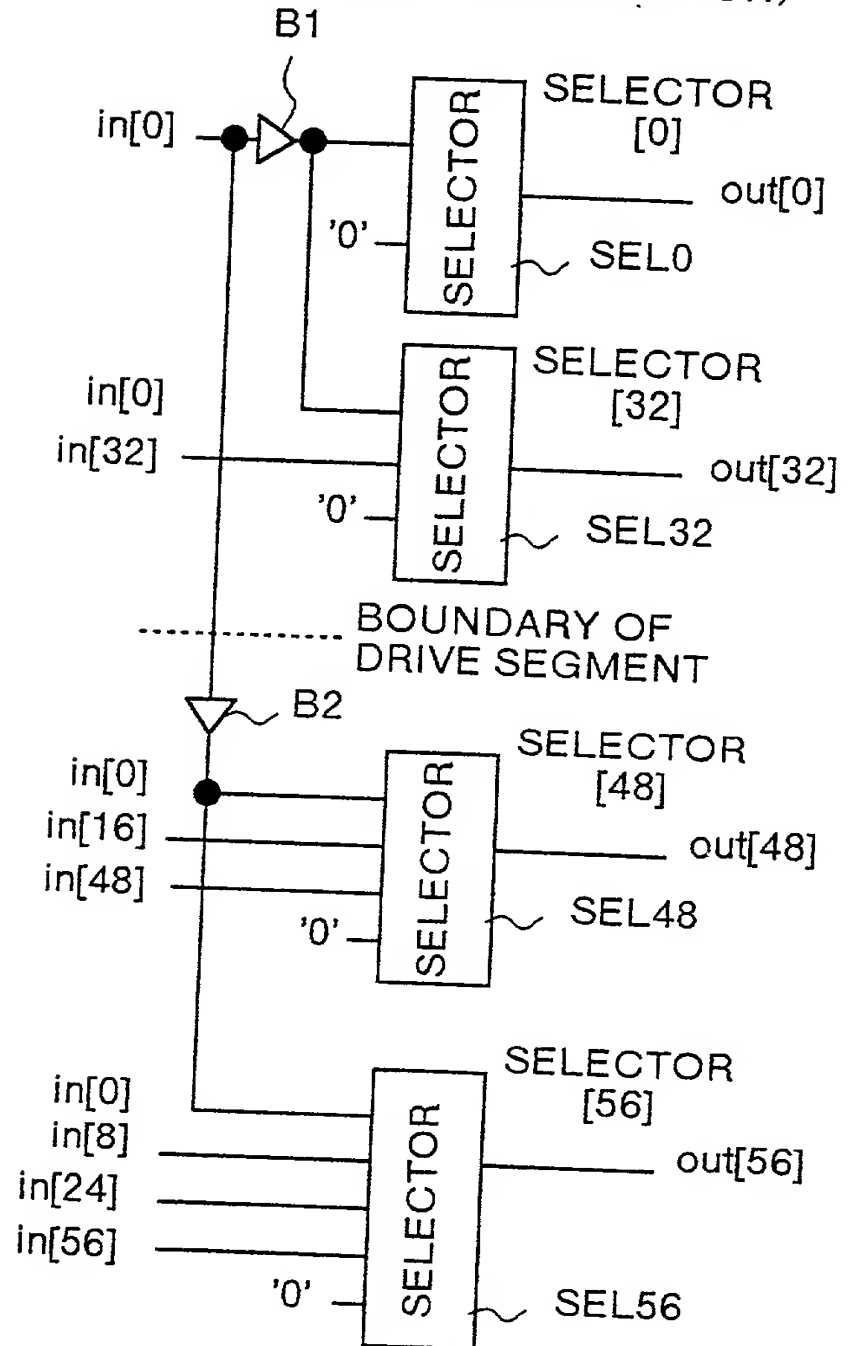
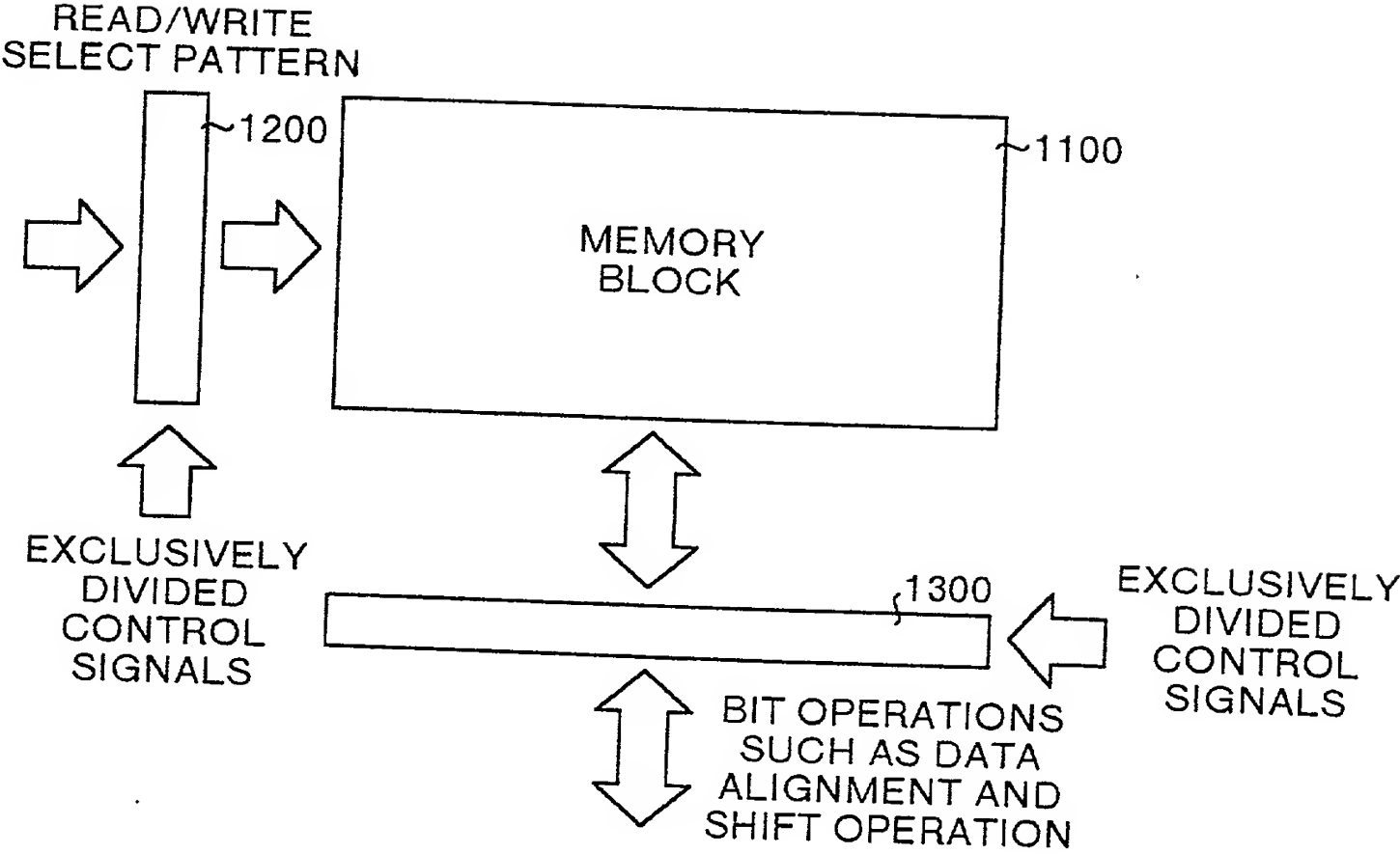


FIG.14

OUTPUT DATA PATTERN										G
	[0:7]	[8:15]	[16:23]	[24:31]	[32:39]	[40:47]	[48:55]	[56:63]		
CONTROL PATTERN 1	all'0'	all'0'	all'0'	all'0'	all'0'	all'0'	all'0'	byte0	byte0	
CONTROL PATTERN 2	all'0'	all'0'	all'0'	all'0'	all'0'	all'0'	byte0	byte1	byte1	
CONTROL PATTERN 3	all'0'	all'0'	all'0'	all'0'	byte0	byte1	byte2	byte3	byte3	
CONTROL PATTERN 4	byte0	byte1	byte2	byte3	byte0	byte1	byte2	byte3	byte3	
CONTROL PATTERN 5	byte0	byte1	byte2	byte3	byte4	byte5	byte6	byte7	byte7	

FIG.15



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FIG.16

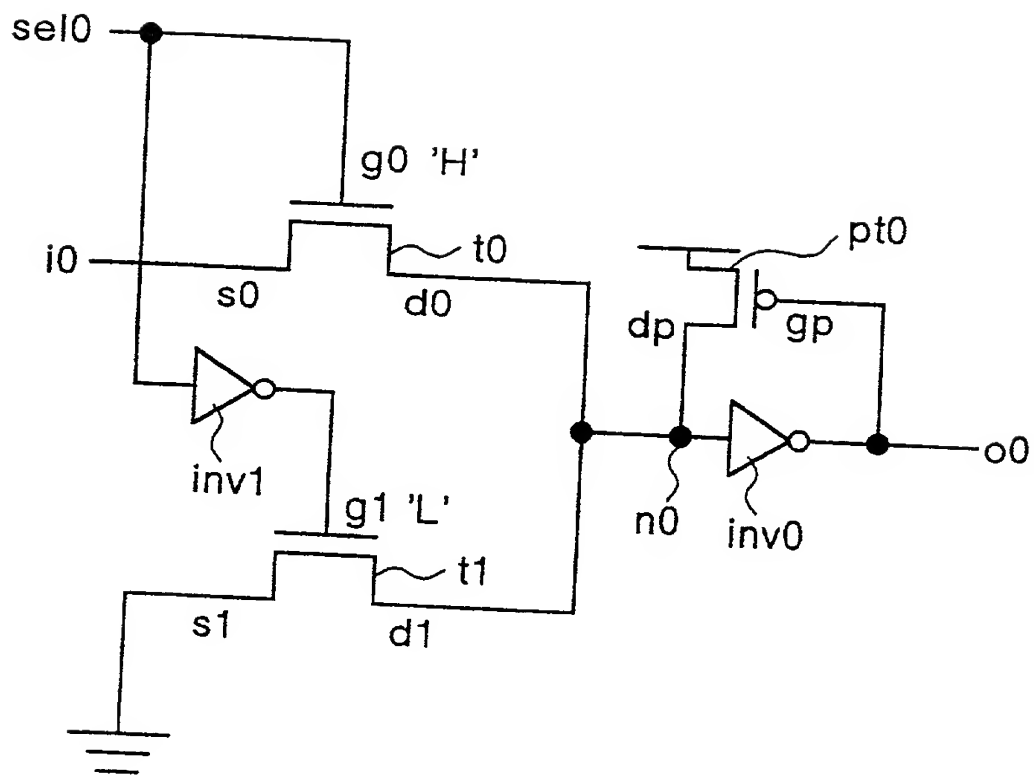


FIG.17

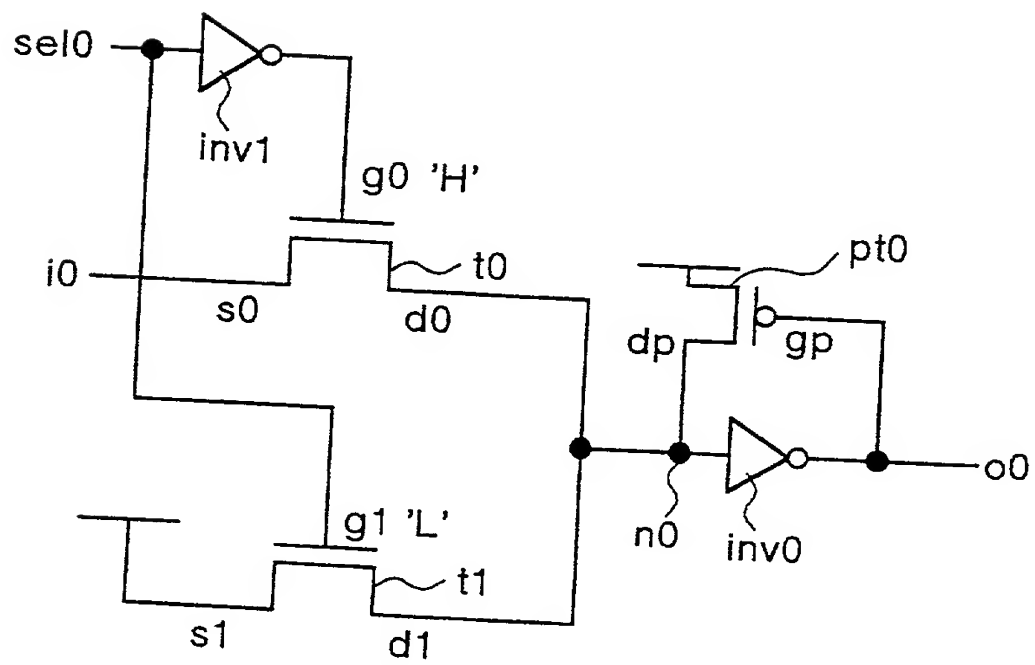


FIG.18

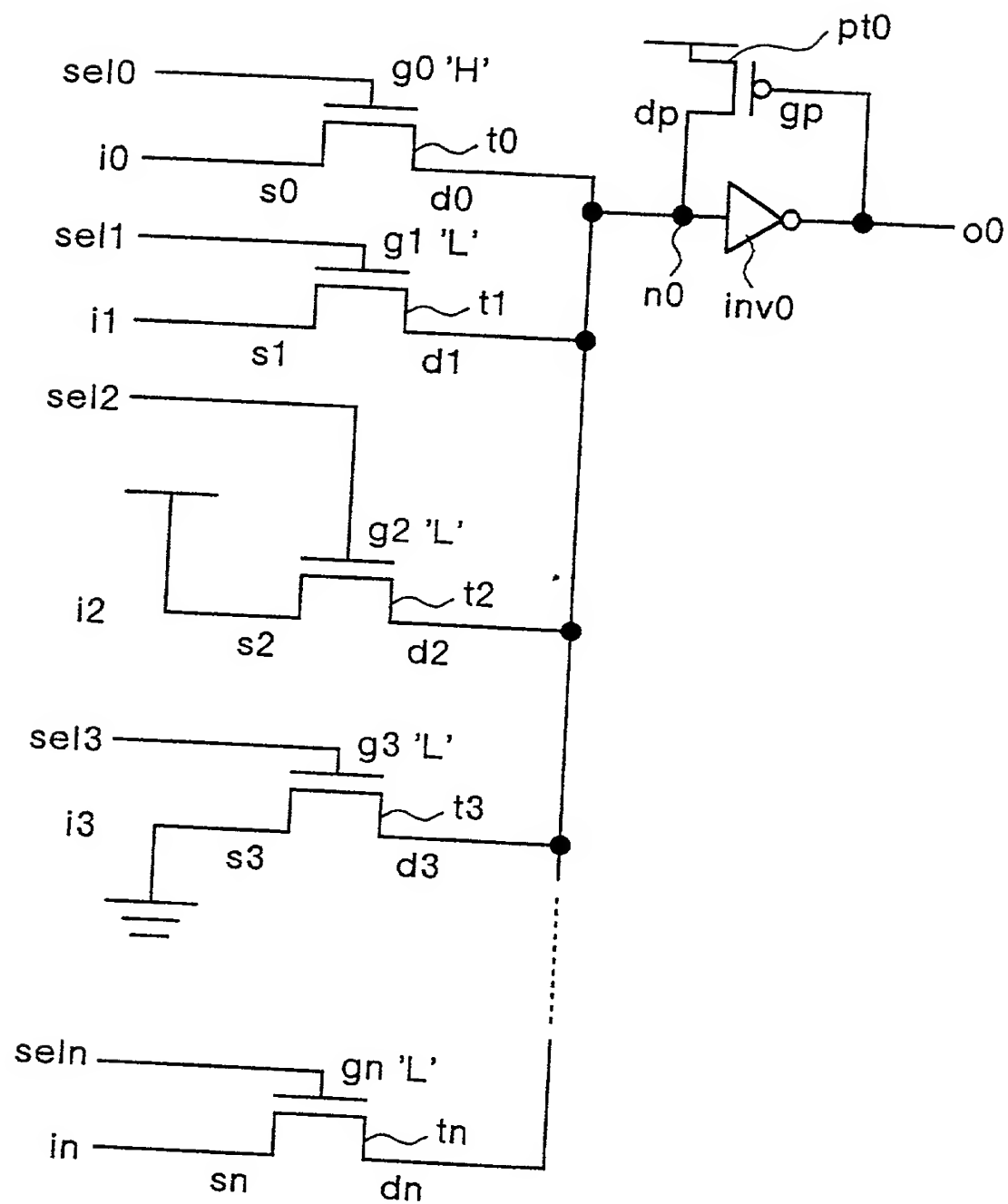


FIG.19

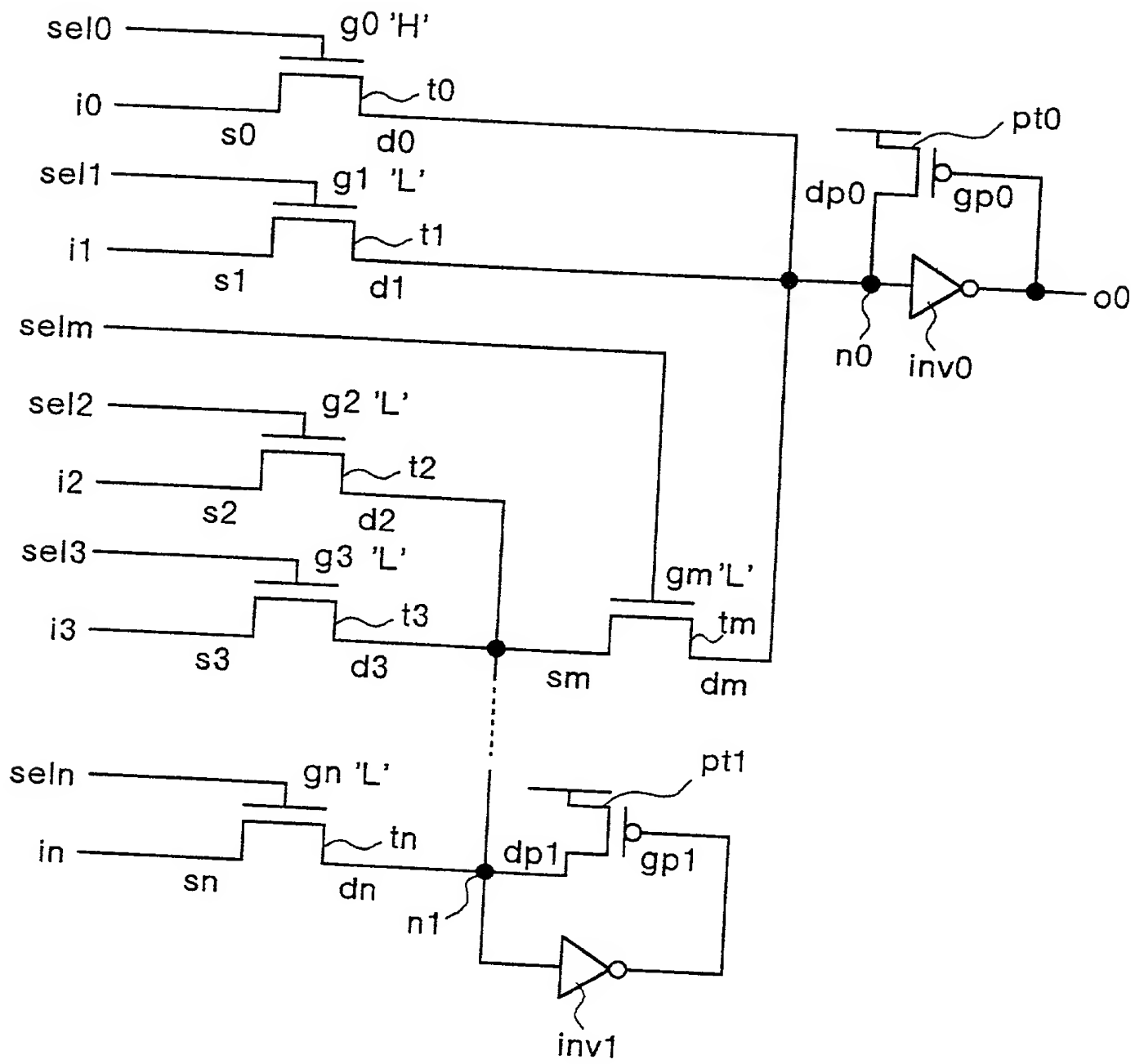


FIG.20

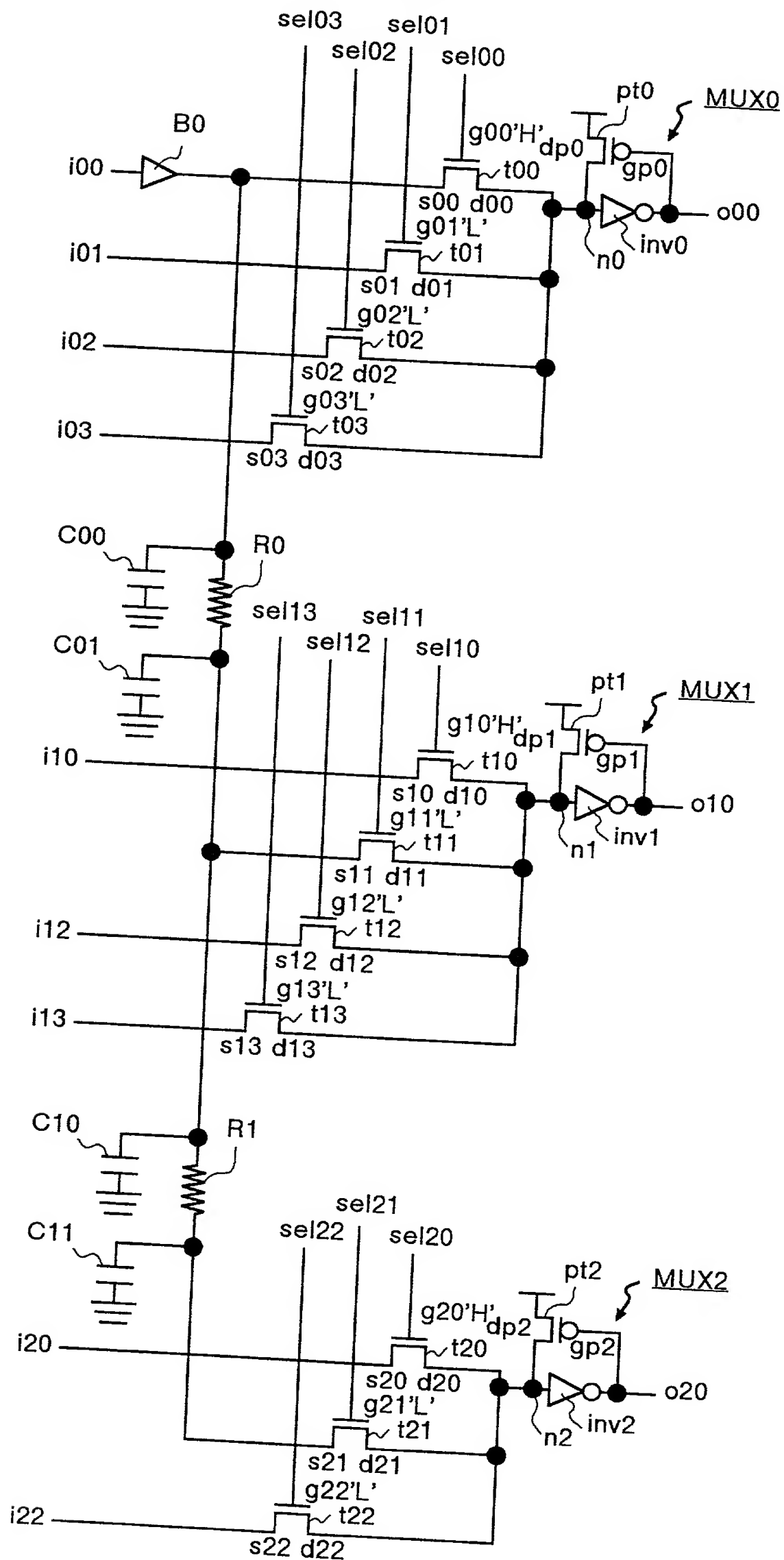


FIG.21

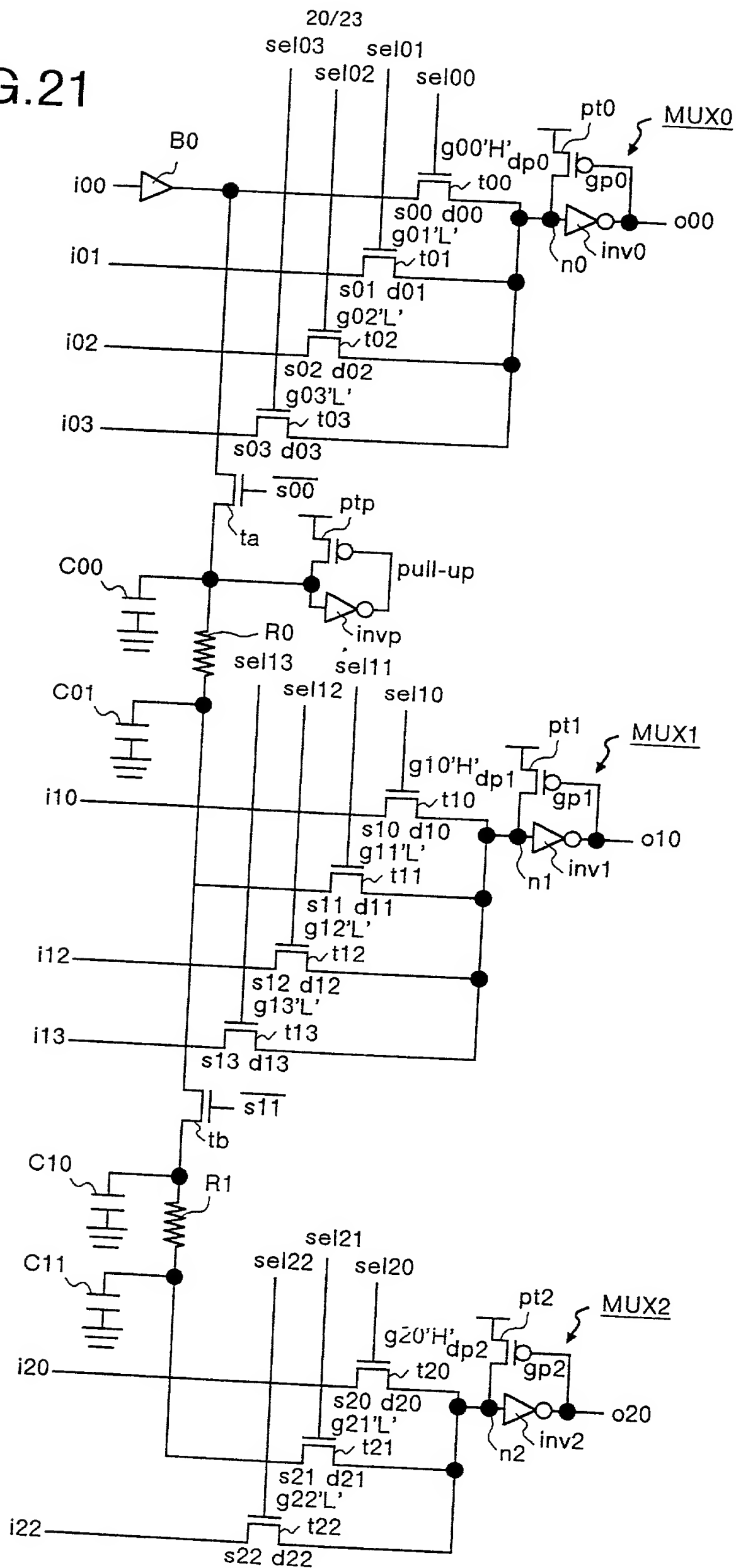


FIG.22

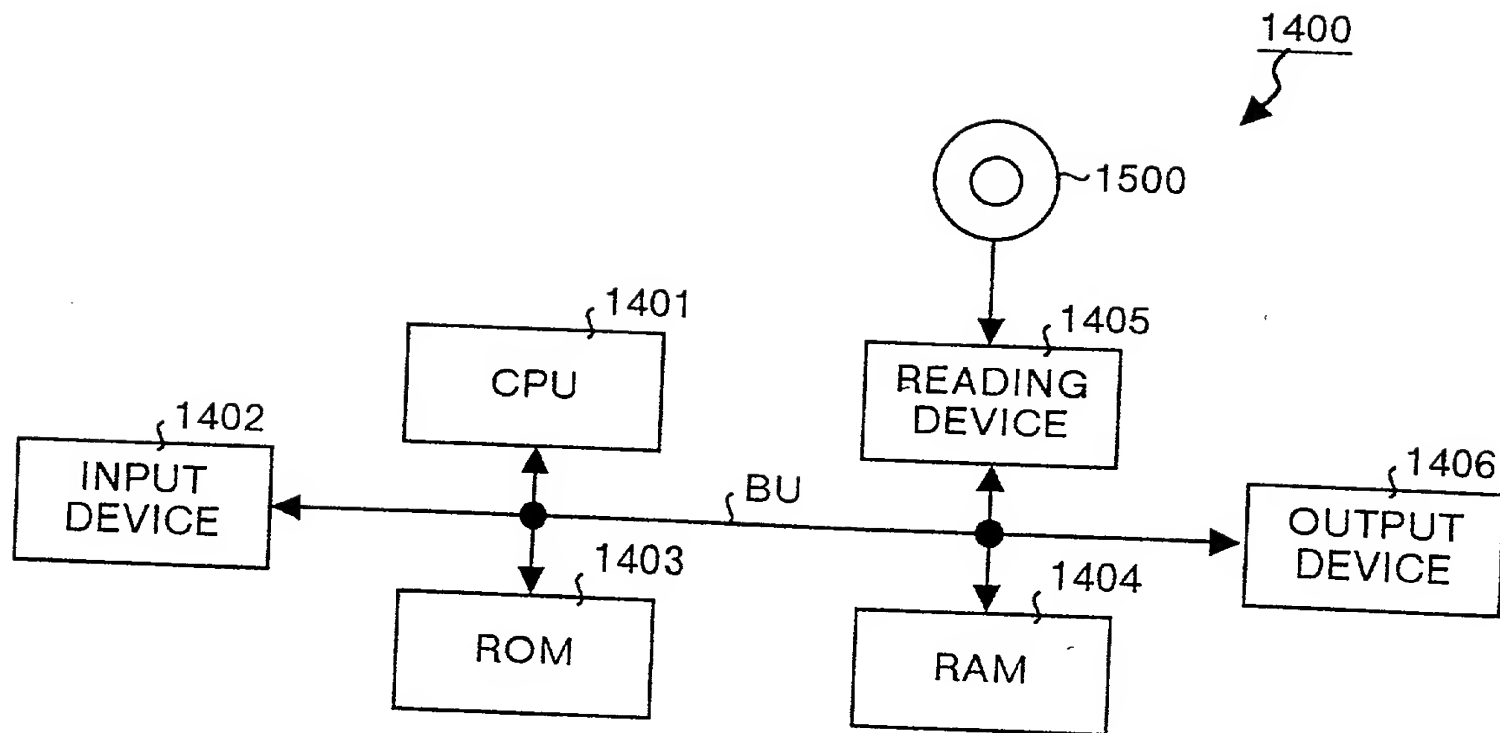


FIG.23A

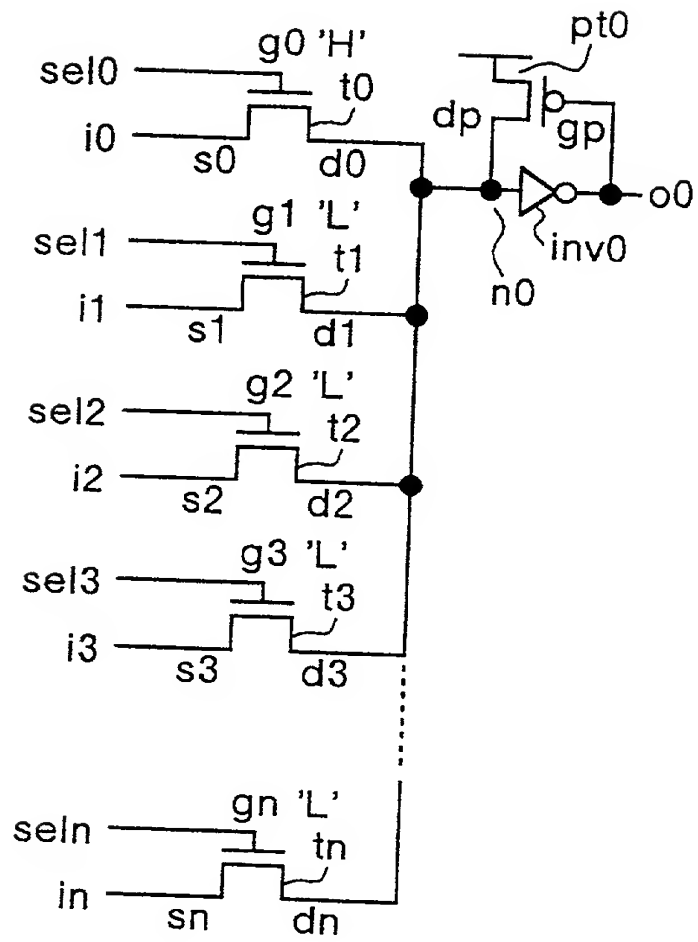


FIG.23B

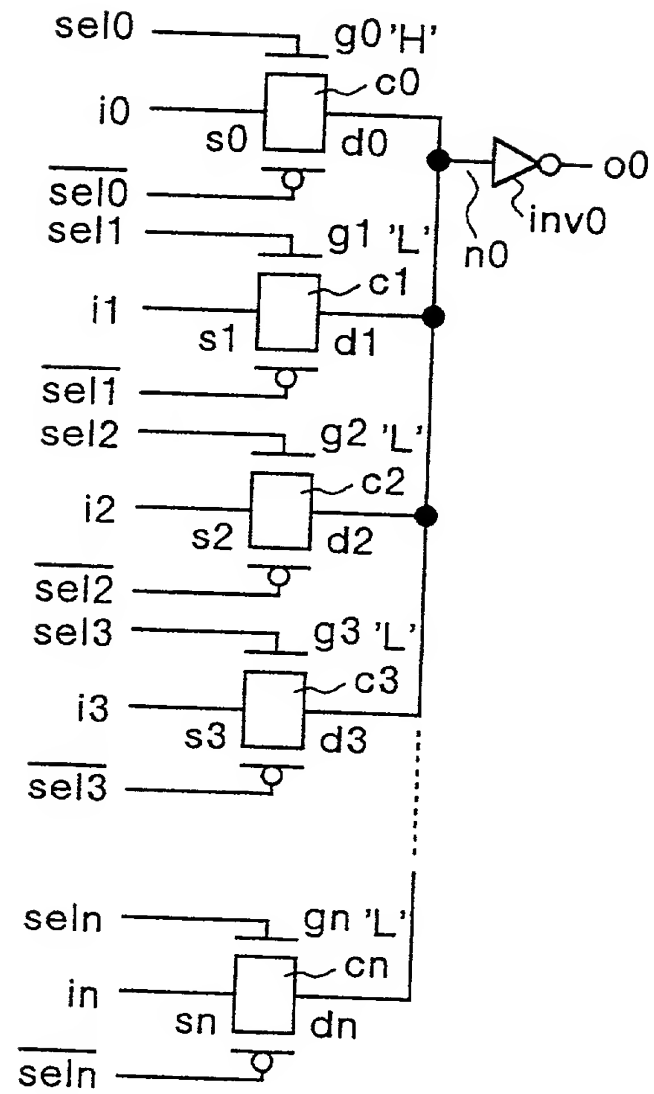


FIG.24

